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Date of Deposit: July 22, 2003

Our Case No.: 10808/33 (01 P 09245)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sasan Cyrusian

Serial No.: 09/865,649

Filing Date: May 25, 2001

For: A GM REPLICA CELL UTILIZING
AN ERROR AMPLIFIER
CONNECTED TO A CURRENT
MIRROR

Examiner: M. Shingleton

Group Art Unit No.: 2817

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SUPPLEMENTAL BB INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Alexandria, VA 22313-1450

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, it is respectfully requested that this Second Supplemental Information Disclosure Statement be entered and the documents listed below and on the attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

The references now cited are the following:

No.	Date	Country
WO 97/42704	November 13, 1997	PCT
0 308 000 A1	September 5, 1988	EPO
0 695 030 A1	January 31, 1996	EPO
1 020 990 A2	July 19, 2000	EPO
"An Analog Front-End Signal Processor for a 64 Mbits/s PRML Hard-Disk Drive Channel," IEEE Journal of Solid-State Circuits, 29, 1994, December, No. 12.		

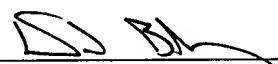


In accordance with 37 C.F.R. § 1.97(g),(h), this Second Supplemental Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

This Supplemental Information Disclosure Statement is being filed prior to the receipt of the first Official Action following a Request For Continued Examination and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with filing of this Supplemental Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925. A duplicate copy of this document is enclosed.

Applicant respectfully requests that the listed documents be made of record in the present case.

Respectfully submitted,


David H. Bluestone
Registration No. 44,542
Attorney for Applicant(s)

BRINKS HOFER GILSON & LIONE
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CERTIFICATE OF MAILING

I certify this correspondence is being deposited with the United States Postal Service as first class mail in an envelope, with sufficient postage, addressed to: Commissioner for Patents, Washington, D.C. 20231, on

August 23, 2001

Date of Deposit

David H. Bluestone

Name of Applicant, Assignee or
Registered Representative

DS BK

Signature

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Docket No.: 01 P 09245 US (10808/33)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Sasan Cyrusian

Examiner: To be assigned

Serial No.: 09/865,649

Group Art Unit No.: 2858

Filing Date: May 25, 2001

For: METHOD AND APPARATUS FOR
CALIBRATING A GM CELL UTILIZING A
REPLICA GM CELL

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56, Applicant respectfully requests this Information Disclosure Statement to be entered and the documents listed below and on the attached Form PTO-1449 be considered by the Examiner and made of record. Copies of the listed documents required by 37 C.F.R. § 1.98(a)(2) are enclosed for the convenience of the Examiner.

The references now cited are the following:

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

"SYNCHRONOUS RECORDING CHANNELS - PRML & BEYOND", rev. 5.61 14.E.18, 1999, published by Knowledge Tek, Inc., Broomfield, Colorado.
"PRML: Seagate Uses Space Age Technology" available on the Internet at http://www.seagate.com/support/kb/disc/prml.html , 2 pages, last accessed 4/9/01
"Technologies - PRML" available on the Internet at http://www.idema.org/about/industry/ind_tech_prml.html , 1 page, last accessed 4/9/01.
"Reference Guide - Hard Disk Drives" available on the Internet at http://www.storagereview.com/guide2000/ref/hdd , 13 pages, last accessed 4/9/01.
"MR AND PRML: TECHNOLOGIES IN SYNERGY" available on the Internet at http://www.lionsgate.com/Home/Baden/public_html_index/SCSI/Quantum_White_Papers/MR_Head/MR , 4 pages, last accessed 4/9/01."
"A Tutorial on Convolutional Coding with Viterbi Decoding" available on the Internet at http://pw1.netcom.com/~chip.f/viterbi/tutorial.html , 10 pages, last accessed 4/9/01.

In accordance with 37 C.F.R. § 1.97(g),(h), this Information Disclosure Statement is not to be construed as a representation that a search has been made and is not to be construed to be an admission that the information cited is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b).

This Information Disclosure Statement is being filed within three (3) months of the filing date and prior to the receipt of the first Official Action reflecting an examination on the merits and hence is believed to be timely filed in accordance with 37 C.F.R. § 1.97(b). No fees are believed to be due in connection with filing of this Information Disclosure Statement, however, should any fees under 37 C.F.R. §§ 1.16 to 1.21 be deemed necessary for any reason relating to these material, the Commissioner is hereby authorized to deduct said fees from Brinks Hofer Gilson & Lione Deposit Account No. 23-1925. A duplicate copy of this document is enclosed.

Applicant respectfully requests that the listed documents be made of record in
the present case.

Respectfully submitted,



David H. Bluestone
Registration No. 44,542
Attorney for Applicant

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Chicago, IL 60610
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Form PTO-1449 (REV. 8-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 01 P 09245 US (10808/33)	SERIAL NO. 09/865,649
INFORMATION DISCLOSURE CITATION <i>(Use several sheets if necessary)</i>		APPLICANT Sasan Cyrusian	EXAMINER TBA
		FILING DATE 5/25/01	GROUP 2858
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U. S. PATENT DOCUMENTS

Examiner INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB-CLASS	TRANSLATION
							YES

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	A1	"SYNCHRONOUS RECORDING CHANNELS - PRML & BEYOND", rev. 5.61 14.E.18, 1999, published by Knowledge Tek, Inc., Broomfield, Colorado.
	A2	"PRML: Seagate Uses Space Age Technology" available on the Internet at http://www.seagate.com/support/kb/disc/prml.html , 2 pages, last accessed 4/9/01
	A3	"Technologies - PRML" available on the Internet at http://www.idema.org/about/industry/ind_tech_prml.html , 1 page, last accessed 4/9/01.
	A4	"Reference Guide - Hard Disk Drives" available on the Internet at http://www.storagereview.com/guide2000/ref/hdd , 13 pages, last accessed 4/9/01.
	A5	"MR AND PRML: TECHNOLOGIES IN SYNERGY" available at on the Internet at http://www.lionsgate.com/Home/Baden/public_html_index/SCSI/Quantum_White_Papers/MR_Head/MR , 4 pages, last accessed 4/9/01."
	A6	"A Tutorial on Convolutional Coding with Viterbi Decoding" available on the Internet at http://pw1.netcom.com/~chip.f/viterbi/tutorial.html , 10 pages, last accessed 4/9/01.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.



FORM PTO-1449	SERIAL NO. 09/865,649	CASE NO. 10808/33 (01 P 09245)
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary)	FILING DATE May 25, 2001	GROUP ART UNIT 2817
	APPLICANT(S): Sasan Cyrusian	

REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	NAME	CLASS/ SUBCLASS	FILING DATE

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER Number-Kind Code (if known)	DATE	COUNTRY	CLASS/ SUBCLASS	TRANSLATION YES OR NO
	BB1	WO 97/42704	November 13, 1997	PCT		
	BB2	0 308 000 A1	September 5, 1988	EPO		
	BB3	0 695 030 A1	January 31, 196	EPO		
	BB4	1 020 990 A2	July 19, 2000	EPO		

EXAMINER INITIAL	OTHER ART – NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.)	
	BB5	"An Analog Front-End Signal Processor for a 64 Mbits/s PRML Hard-Disk Drive Channel," IEEE Journal of Solid-State Circuits, 29, 1994, December, No. 12.

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